

SCHOLARSHIP PROGRAM

Global Semiconductor Exchange Program (GSEP)

Location: PFCC, Malaysia

The 8-week Global Semiconductor Exchange Program (GSEP) offers hands-on training in IC design, manufacturing, and testing. Participants gain skills in digital IC structures, analog layout design, 3D-simulated manufacturing, chip testing, mastering simulation, debugging, layout design, and test programming for real-world IC development challenges.

Testing & Verification Training

Deadline: 28th February 2025

Program Structure

- Introduction to Digital & Analog Integrated Circuit (IC) Design
- Introduction to IC Manufacturing Process & Advanced IC Manufacturing Techniques
- Introduction to IC Packaging Process
 - ❑ Front-End IC Packaging
 - ❑ Back-End IC Packaging
- IC Testing
 - ❑ Shift Register Chip Parameter Testing
 - ❑ Memory Chip Parameter Testing
 - ❑ Power Management Chip Parameter Testing
 - ❑ Operational Amplifier (Op-Amp) Chip Parameter Testing
 - ❑ Microcontroller (MCU) Chip Parameter Testing
 - ❑ Analog-to-Digital Converter (ADC) Mixed-Signal Chip Parameter Testing
 - ❑ Testing Development Standards
 - ❑ Common Fault Analysis
 - ❑ Calibration and Report Writing
 - ❑ Case Study

Intake

April 2025

Duration

2 Months / 8 Weeks

Location

PFCC, Puchong, Selangor, Malaysia.

Career Prospects

- Failure Analysis Engineer
- Quality Assurance (QA) Engineer
- Field Applications Engineer (FAE)
- IC Layout Designer
- Electrical Test Engineer
- Design Verification Engineer
- Circuit Design Engineer
- Physical Design Engineer

Entry Requirements

1. Open to:
 - a. Year 3 and Year 4 undergraduates
 - b. Recent graduates, new hires in E&E, CS, or related fields
2. Following the screening, there will be a mini-exam and an interview to further assess your application.
 - a. Mini-Exam: Topics include Digital Logic, Analog Circuits, General Programming, and Semiconductor Electronics.
 - b. Interview: Focused on the theme "Getting to Know You".
3. Program Deposit:
 - a. A refundable deposit of RM2,000 is required.
 - b. All deposits will be fully refunded upon successful completion of the program.

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Strategic partners



Industry partners



Ecosystem partners



SCHOLARSHIP PROGRAM

Global Semiconductor Exchange Program (GSEP)

Location: Shenzhen, China

The Global Semiconductor Exchange Program (GSEP) is an 8-week intensive training initiative designed to fast-track Year 3 and Year 4 undergraduate students into high-demand roles with industry-relevant skills from the fundamentals of front-end architecture design to circuit design and the complexities of back-end physical design, verification, and layout.

IC DESIGN PROGRAM

Deadline: 28th February 2025

Program Structure

- Introduction to the IC Industry and Digital Design
 - Overview of the Integrated Circuit Industry
 - Overview of Digital Design
- SPI Data Transmission Module Design and Practice
 - Logic Synthesis Implementation and Practice
- Verification and Testing
 - Formal Verification Design for
 - Testability (DFT)
- Physical Design and Verification
 - Layout and Routing Physical
 - Verification RC Parameter
 - Extraction Static Timing
 - Analysis
- ADC Chip Design Implementation and Practice
- Processor and SOC Design
- Capstone Project for Each Major Module
 - ADC Chip Design Capstone RISC-V
 - SOC Design Capstone
- Final Exam for Each Major Module
 - Theoretical and practical evaluations

Intake

April 2025

Duration

2 Months / 8 Weeks

Location

Shenzhen Information Vocational Technology College, China.

Career Prospects

- Logic Design Engineer
- Design Verification Engineer
- Circuit Design Engineer
- Physical Design Engineer
- Software Engineer
- Design Automation Engineer
- Silicon Package Design Engineer

Entry Requirements

1. Open to:
 - a. Year 3 and Year 4 undergraduates
 - b. Recent graduates, new hires in E&E, CS, or related fields
2. Following the screening, there will be a mini-exam and an interview to further assess your application.
 - a. Mini-Exam: Topics include Digital Logic, Analog Circuits, General Programming, and Semiconductor Electronics.
 - b. Interview: Focused on the theme "Getting to Know You".
3. Program Deposit:
 - a. A refundable deposit of RM3,000 is required.
 - b. All deposits will be fully refunded upon successful completion of the program.

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